**Mmic Pcb**

Features:-

10-bit resolution and 14 channels

12-bit resolution and 28 channels

 8-bit Timer/Counter with 8-bit
24/35 I/O Pins with Individual Direction Control:
High current source/sink for direct LED drive
Interrupt-on-Change pin
Individually programmable weak pull-ups
Ultra Low-Power Wake-up (ULPWU)

Power-Managed modes:
Run: CPU on, peripherals on
Idle: CPU off, peripherals on
Sleep: CPU off, peripherals off
Two-Speed Oscillator Start-up
Fail-Safe Clock Monitor
Power-Saving Peripheral Module Disable (PMD)
Ultra Low-Power Wake-up
Fast Wake-up, 1 μs Typical
Low-Power WDT, 300 nA Typical
Ultra Low 50 nA Input Leakage
Run mode Currents Down to 5.5 μA, Typical
Idle mode Currents Down to 1.7 μA Typical
Sleep mode Currents Down to Very Low 20 nA,

Rs232 Custom based Protocol TCP/IP/UDP